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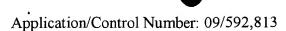
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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	ATTORNEY DOCKET NO. CONFIRMATION NO.	
09/592,813	06/13/2000	Philip Piro	1920/106	1920/106 7669	
2101	7590 03/26/2003				
BROMBERG & SUNSTEIN LLP 125 SUMMER STREET			EXAMINER		
BOSTON, MA 02110-1618			LEI, TSULEUN R		
			ART UNIT	PAPER NUMBER	
			2684	<u>i</u>	
			DATE MAILED: 03/26/2003	Θ	

Please find below and/or attached an Office communication concerning this application or proceeding.



	Application No.	Applicant(s)
	09/592,813	PIRO ET AL.
Office Action Summary	Examiner	Art Unit
TI STAN INCO DATE - Eddi-	T. Richard Lei	2684
The MAILING DATE of this communication appe Period for Reply	ears on the cover sheet with the c	orrespondence address
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.130 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply of the NO period for reply is specified above, the maximum statutory period with Failure to reply within the set or extended period for reply will, by statute, and any reply received by the Office later than three months after the mailing of earned patent term adjustment. See 37 CFR 1.704(b). Status	6(a). In no event, however, may a reply be tim within the statutory minimum of thirty (30) days Il apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE!	nely filed s will be considered timely. the mailing date of this communication. O (35 U.S.C. § 133).
1) Responsive to communication(s) filed on		
2a) ☐ This action is FINAL . 2b) ☑ This	s action is non-final.	
Since this application is in condition for alloware closed in accordance with the practice under E Disposition of Claims		
4)⊠ Claim(s) <u>1-20</u> is/are pending in the application.		
4a) Of the above claim(s) is/are withdraw	n from consideration.	
5) Claim(s) is/are allowed.		
6)⊠ Claim(s) <u>1-20</u> is/are rejected.		
7) Claim(s) is/are objected to.		
8) Claim(s) are subject to restriction and/or	election requirement.	
Application Papers		
9) The specification is objected to by the Examiner.		
10) The drawing(s) filed on is/are: a) accept		
Applicant may not request that any objection to the	- · ·	` '
11) The proposed drawing correction filed on		ved by the Examiner.
If approved, corrected drawings are required in repl		
12) The oath or declaration is objected to by the Exa	miner.	
Priority under 35 U.S.C. §§ 119 and 120		
13) Acknowledgment is made of a claim for foreign	priority under 35 U.S.C. § 119(a))-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:		
1. Certified copies of the priority documents		
2. Certified copies of the priority documents		
 3. Copies of the certified copies of the priorit application from the International Bure * See the attached detailed Office action for a list of the priority 	eau (PCT Rule 17.2(a)).	•
14)☐ Acknowledgment is made of a claim for domestic		
a) ☐ The translation of the foreign language prov 15)☐ Acknowledgment is made of a claim for domestic	isional application has been rec	eived.
Attachment(s)		,
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 4.		(PTO-413) Paper No(s) Patent Application (PTO-152)



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DETAILED ACTION

Claim Rejections - 35 USC § 102

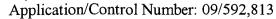
1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 2. Claims 1-20 are rejected under 35 U.S.C. 102(e) as being anticipated by Andrys et al (U.S. Patent 6,057,714).

Regarding Claim 1, Andrys teaches a field effect transistor mixer comprising:

- a) a balun having a primary and a secondary, the primary coupled to a radio frequency signal input (Fig.1, RF balun 2);
- b) a pair of field effect transistors, each transistor having a gate, a source, a drain, and a channel between the source and the drain, wherein i). the gates of the transistors are coupled to one another and to a local oscillator input (Fig.1, Mixer 4), ii). one of the source and the drain of a first of the two transistors is coupled at a node to one of the source and the drain of the other of



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the two transistors, and the node is coupled to ground (Fig. 1, Mixer 4, 54 & 56), iii). the other of the source and the drain of the first of the two transistors is coupled to one side of the secondary of the balun and the other of the source and the drain of the second of the two transistors is coupled to the other side of the secondary of the balun (Fig. 1);

c). and an intermediate frequency signal output coupled to a point in the circuit path between the first and second transistors (Fig.1).

Regarding Claim 2, Andrys teaches a mixer according to claim 1, wherein, at the node, one of the source and the drain of the second of the two transistors is connected to ground, and the node is coupled to one of the source and drain of the first of the two transistors by a filter (Fig.1, Mixer 4, 54 & 56; Official notice: if the IF output is single-ended, instead of balanced as shown in Fig.1, one node would be grounded).

Regarding Claim 3, Andrys teaches a mixer according to claim 2, the filter having a capacitor serving as a shunt at the frequency of the Radio frequency signal but not at the frequency of the intermediate frequency signal (Fig.1, Mixer 4, 54 & 56).

Regarding Claim 4, Andrys teaches a mixer according to claim 1, wherein the local oscillator input is coupled to the gates via a capacitor so as to cause the transistors to be biased near pinch off (Fig. 1 Bias circuit 6; Col. 2, Lines 2-16).



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Regarding Claim 5, Andrys teaches a mixer according to claim 1, further comprising a capacitance disposed across the balun secondary to tune the secondary (Fig.1, 24; Col.4, Lines 7-16).

Regarding Claim 6, Andrys teaches a mixer according to claim 5, wherein the capacitance includes a pair of capacitors connected in series, wherein the node at a connection between the capacitors is coupled to ground (Fig.1, 24).

Regarding Claim 7, Andrys teaches a mixer according to claim 1, wherein the secondary has a center tap, and the center tap is coupled to ground (Fig. 1, 24).

Regarding Claim 8, see Claim 1 for Andrys teaching.

Regarding Claim 9, see Claim 2 for Andrys teaching.

Regarding Claim 10, see Claim 3 for Andrys teaching.

Regarding Claim 11, Andrys teaches a method according to claim 8, further comprising applying a capacitance across the balun primary to tune the primary (Fig. 1, 22; Col. 4, Lines 7-16).

Regarding Claim 12, see Claim 5 for Andrys teaching.

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Regarding Claim 13, see Claim 6 for Andrys teaching.

Regarding Claim 14, see Claim 7 for Andrys teaching.

Regarding Claim 15, see Claim 1 for Andrys teaching.

Regarding Claim 16, see Claim 2 for Andrys teaching.

Regarding Claim 17, see Claim 3 for Andrys teaching.

Regarding Claim 18, see Claim 5 for Andrys teaching.

Regarding Claim 19, see Claim 6 for Andrys teaching.

Regarding Claim 20, see Claim 11 for Andrys teaching.

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

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Dobrovolny (U.S. patent 5,280,648) teaches a double-balance RF mixer.

Vice (U.S. Patent 5,799,248) teaches a quasi-double-balanced FET mixer.

Nice et al. (U.S. patent 6,144,236) teaches a super FET mixer.

Waugh et al. (U.S. Patent 5,060,298) teaches a monolithic double-balanced mixer.

Poulin et al. (U.S. Patent 6,278,872) teaches a frequency converter with improved linearity.

Any inquiry concerning this communication or earlier communications from the 4. examiner should be directed to T. Richard Lei whose telephone number is 703-305-4828. The examiner can normally be reached on 8:30 to 5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dwayne Bost can be reached on 703-305-4778. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-5403 for regular communications and 703-308-5403 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-305-3900.

March 18, 2003

THANH CONG LE PRIMARY EXAMINER